

Avocet II

The 4th Generation DAC

The 4th Generation D/A for Avocet uses an extremely low jitter reference for jitter reduction and a unique combination of analog and digital reconstruction filters. The reference oscillator uses techniques that are borrowed from microwave communication and test instruments in it's design, and is the result of 2 years of research. The DAC is the latest generation AKM 32 bit part.

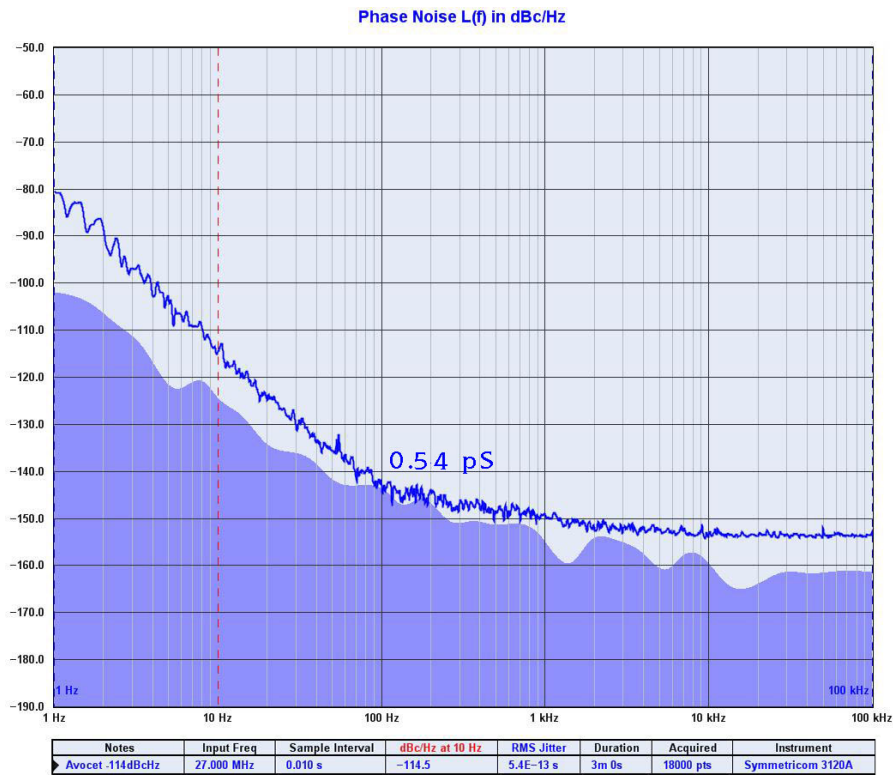
The result is a DAC with unsurpassed imaging and transit response. This is integrated into Avocet's analog electronics and digital control systems.

The difference between Avocet and Avocet II is the DAC



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PHASE NOISE - JITTER MEASUREMENT FOR THE 4TH GENERATION DAC



Jitter measurement notes:

This is a measurement of the reference oscillator used for the ASRC in the jitter reduction circuit. Oscillators that measure above 1pS are rejected. The measurement requires a 25 minute warm up period.

The Symmetricom 3120A Phase noise probe was used for the measurement, with a Stanford Research Systems Rubidium reference.

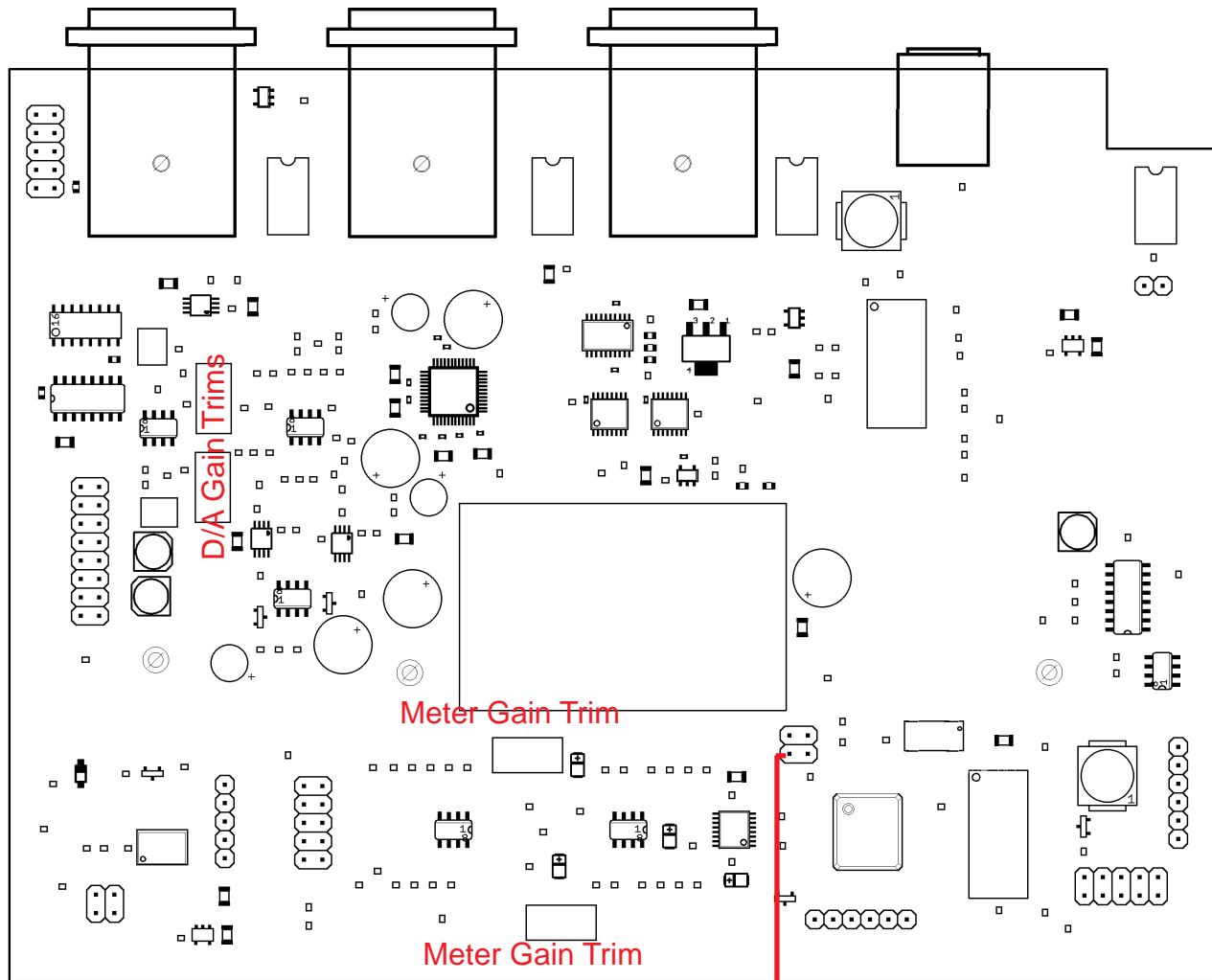
From 30Hz to 3 KHz the Rubidium is not a good enough reference, the phase noise in that range is most likely lower than the plot.

Test Software does not make a distinction between random and deterministic jitter. AES-12id-2006 (r2011) excludes modulation components below 10 Hz in its jitter definition.

This measurement is from 1 Hz to 100 KHz

Jitter requirements depends on the circuits being used and the desired results.

4TH GENERATION DAC PCB SHOWING GAIN TRIMS AND JUMPERS



Meter Master = jmp on
Surround Slave = jmp off