

# M7

Bricasti Design



## Digital Section Design Features and Performance (preliminary)

- AES 24 Bit Digital I/O
- AES single wire 192k supported
- Self-clocking to incoming sample rates 44.1k to 192k
- 6 State of the art dual core Analog Devices DSPs

## Analog Section Design Features and Performance (preliminary)

- < 20 Picoseconds Jitter
- Input level adjustable in 2 dB steps
- Balanced XLR analog I/O
- 24-bit conversion
- Dual D/A
- Fully-balanced, DC-coupled analog input and output circuits

### Analog Input A/D

- ▶ Dynamic Range >116 db, A-Weighted
- ▶ THD+N <.001 %
- ▶ Frequency Response: 10 Hz - 20 kHz  $\pm$  < 0.05 dB
- ▶ CMRR: > 60 dB @  $\leq$  1 kHz
- ▶ Maximum Input Level: +24 dBu
- ▶ Minimum Input Level for 0 dBFS: +4 dBu

### Analog Output D/A

- ▶ Dynamic Range: > 116 dB, A-Weighted
- ▶ THD+N: < 0.001%
- ▶ Frequency Response: 10 Hz - 20 kHz  $\pm$  < 0.05 dB
- ▶ Maximum Output Level: +24 dBu
- ▶ Minimum Output Level for 0 dBFS: +8 dBu
- ▶ Output impedance: 40 Ohms

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